

60V, 50A DUAL N CHANNEL POWER MOSFET

GENERAL DESCRIPTION

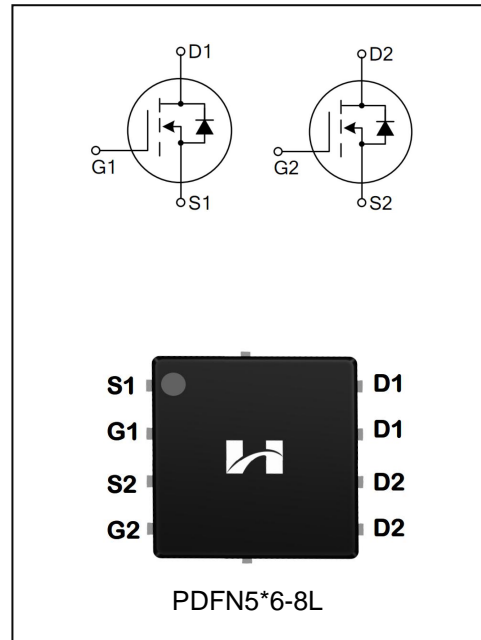
The SFM6005DT uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety applications.

Features

- ◆  $V_{DS}=60V, I_D=50A$
- ◆  $R_{DS(on)}$   
 TYP:  $13.5m\Omega @ V_{GS}=10V$   
 TYP:  $18.5m\Omega @ V_{GS}=4.5V$

Applications

- ◆ Power faction correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SFM6005DT	PDFN5*6	SFM6005DT	Pb Free	Reel

## ABSOLUTE MAXIMUM RATINGS (T<sub>J</sub>=25°C unless otherwise noted)

Characteristics		Symbol	Ratings	Unit
Drain-Source Voltage		V <sub>DS</sub>	60	V
Gate-Source Voltage		V <sub>GS</sub>	±20	V
Drain Current	T <sub>C</sub> = 25°C	I <sub>D</sub>	50	A
	T <sub>C</sub> = 100°C		32	
Drain Current Pulsed(Note 1)		I <sub>DM</sub>	180	A
Power Dissipation(T <sub>C</sub> =25°C)		P <sub>D</sub>	65	W
-Derate above 25°C			0.37	W/°C
Single Pulsed Avalanche Energy (Note 2)		E <sub>AS</sub>	270	mJ
Operation Junction Temperature Range		T <sub>J</sub>	-55~+150	°C
Storage Temperature Range		T <sub>stg</sub>	-55~+150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		TL	300	°C

## THERMAL CHARACTERISTICS

Characteristics	Symbol	MAX	Unit
Thermal Resistance, Junction-to-Case	R <sub>θJC</sub>	3.1	°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	50	°C/W

## ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
Drain -Source Breakdown Voltage	B <sub>VDS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	60	--	--	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	--	--	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =20V, V <sub>DS</sub> =0V	--	--	100	nA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V	--	--	-100	
<b>On Characteristics</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> =250μA	1.4	1.9	2.5	V
Static Drain- Source On State Resistance	R <sub>D(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	--	13.5	20	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	--	18.5	29	
<b>Dynamic Characteristics</b>						
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V; f=1.0MHZ	1	5	10	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V V <sub>GS</sub> =0V f=1.0MHZ	--	1830	--	pF
Output Capacitance	C <sub>oss</sub>		--	115	--	
Reverse Transfer Capacitance	C <sub>rss</sub>		--	105	--	
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =30V, V <sub>GS</sub> =10V R <sub>GS</sub> =3.3Ω, I <sub>D</sub> =20A (Note 3.4)	--	16.2	--	ns
Turn-on Rise Time	t <sub>r</sub>		--	69.8	--	
Turn-off Delay Time	t <sub>d(off)</sub>		--	173.5	--	
Turn-off Fall Time	t <sub>f</sub>		--	75.3	--	

Total Gate Charge	$Q_g$	$V_{DS}=48V, I_D=20A$ $V_{GS}=10V$ (Note 3.4)	--	46	--	nc
Gate-Source Charge	$Q_{gs}$		--	9.5	--	
Gate-Drain Charge	$Q_{gd}$		--	10.2	--	

## SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	$I_S$	Integral Reverse P-N Junction Diode in the MOSFET	--	--	60	A
Pulsed Source Current	$I_{SM}$		--	--	180	
Diode Forward Voltage	$V_{SD}$	$I_S=20A, V_{GS}=0V$	--	0.85	1.2	V
Reverse Recovery Time	$T_{rr}$	$I_F=20A, V_R=15V,$ $dI_F/dt=100A/\mu S$	--	69	--	ns
Reverse Recovery Charge	$Q_{rr}$		--	26	--	nC

1. Pulse width limited by maximum junction temperature
2.  $L=5mH, V_{DD}=50V, V_G=10V, R_G=25\Omega$ , starting  $T_J=25^\circ C$
3. Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$
4. Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. Output Characteristics

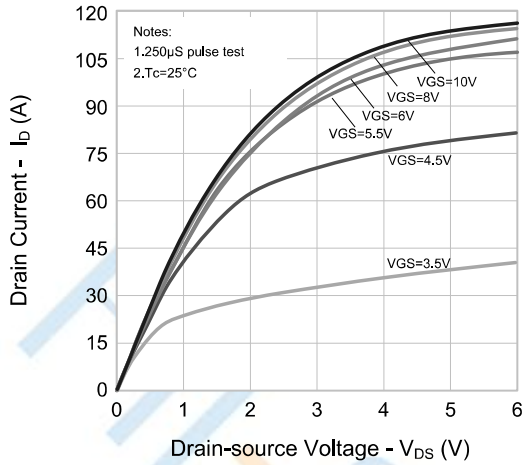


Figure 2. Transfer Characteristics

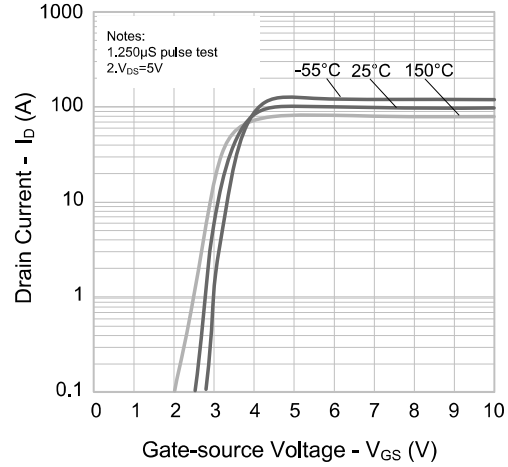


Figure 3. On-resistance vs. Drain Current

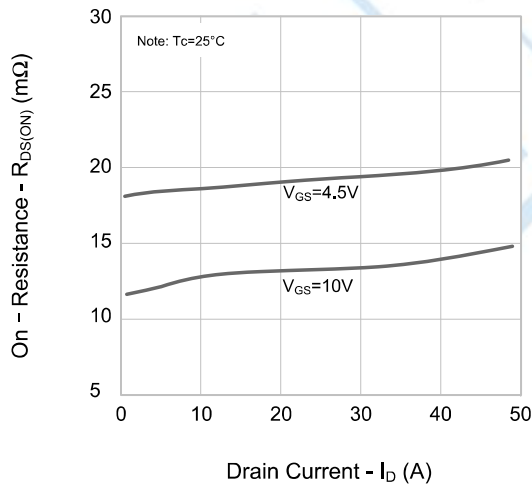


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

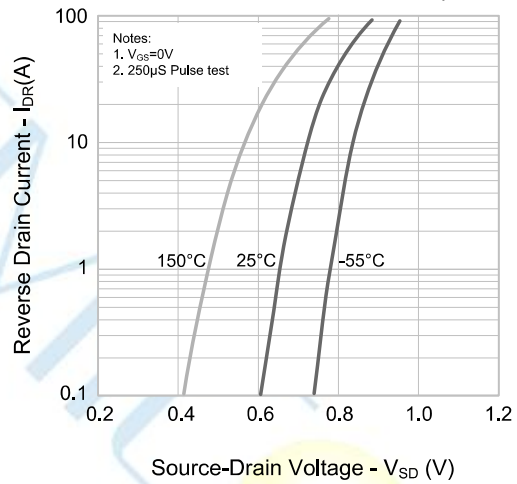


Figure 5. Capacitance Characteristics

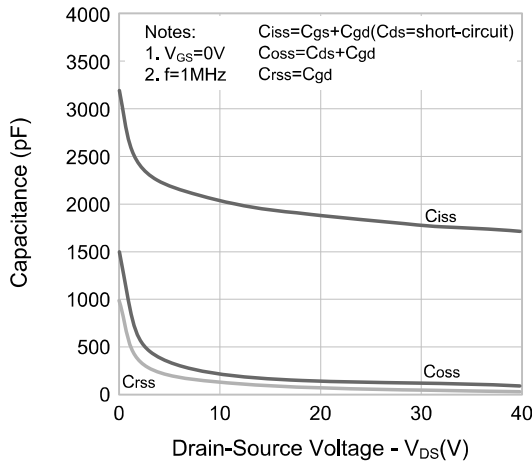
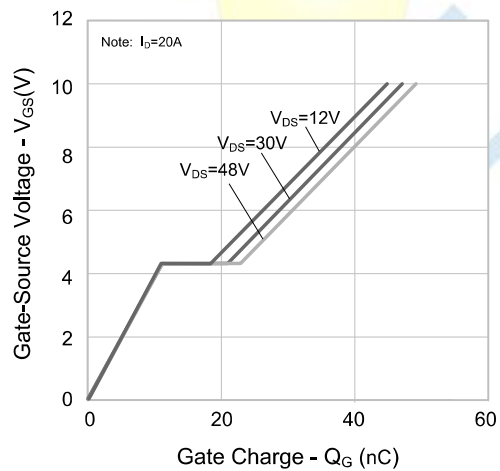


Figure 6. Gate Charge



Typical Performance Characteristics

Figure 7. Breakdown Voltage vs. Temperature Characteristics

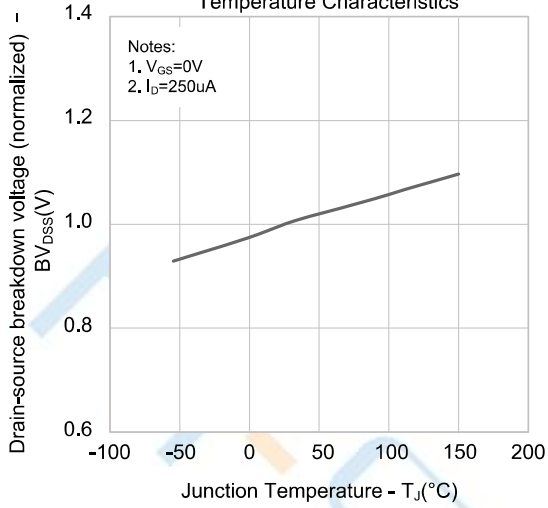


Figure 8. On-resistance vs. Temperature Characteristics

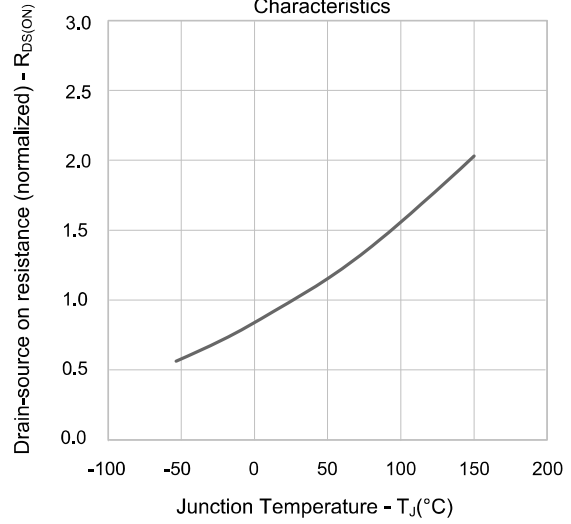
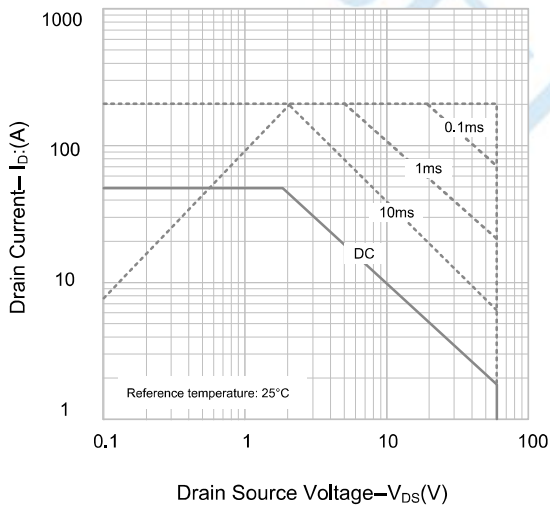
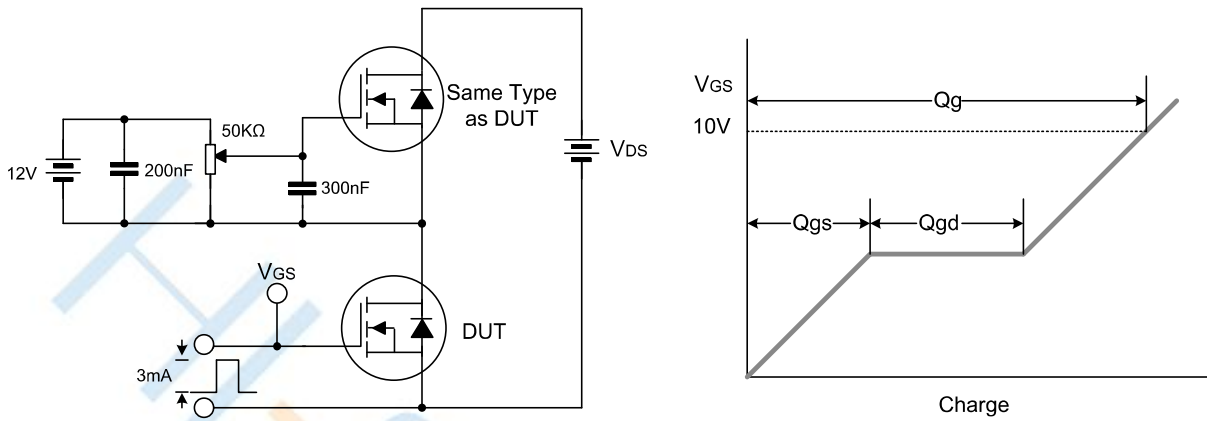


Figure 9. Max. Safe Operating Area

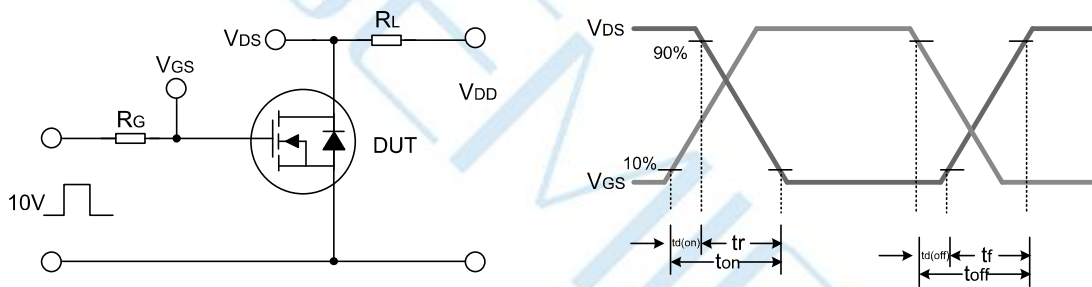


Test Circuit

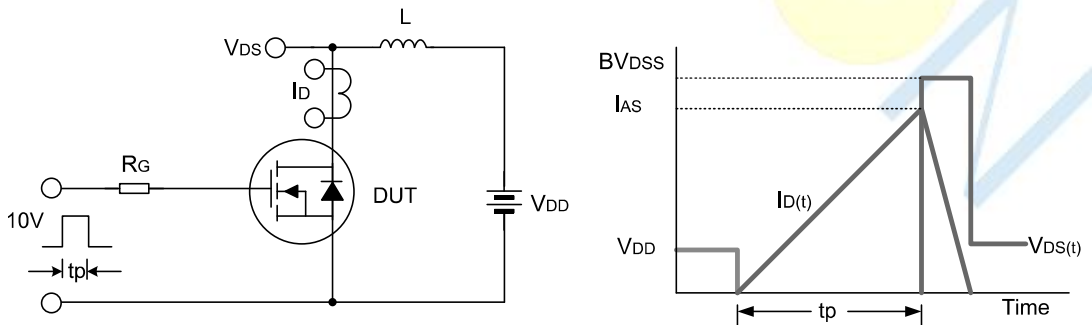
Gate Charge Test Circuit & Waveform



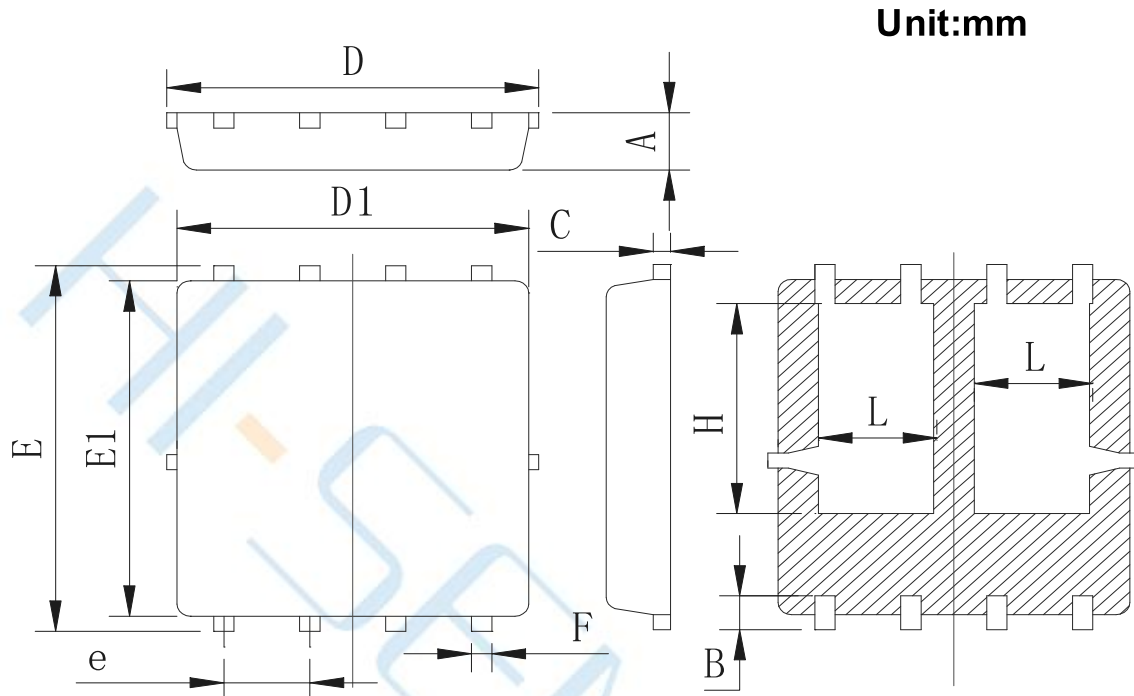
Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform



Package Dimensions of PDFN5\*6-8L



Symbol	Min	Typ	Max
A	0.90	0.95	1.00
B	0.48	0.58	0.68
C	0.234	0.254	0.282
D	5.40	5.50	5.60
D1	5.10	5.20	5.30
E	5.95	6.05	6.15
E1	5.45	5.55	5.65
e	1.22	1.27	1.32
F	0.25	0.30	0.35
H	3.37	3.47	3.57
L	1.60	1.70	1.80

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