

40V, 65A N-CHANNEL POWER MOSFET

GENERAL DESCRIPTION

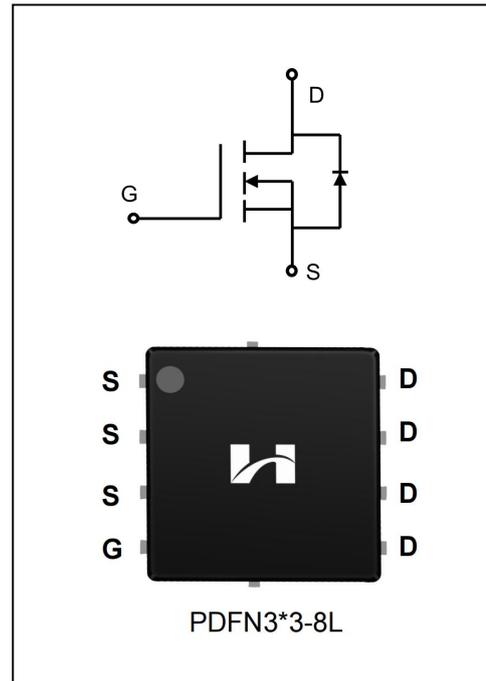
The SFN4006T5 uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety applications.

Features

- ◆ $V_{DS}=40V, I_D=65A$
- ◆ $R_{DS(on)}$
TYP: $5.2m\Omega @ V_{GS}=10V$

Applications

- ◆ Power faction correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

| Part No. | Package | Marking | Material | Packing |
|-----------|------------|-----------|----------|---------|
| SFN4006T5 | PDFN3*3-8L | SFN4006T5 | Pb Free | Reel |

ABSOLUTE MAXIMUM RATINGS (T_J=25°C unless otherwise noted)

| Characteristics | | Symbol | Ratings | Unit |
|---|------------------------|------------------|----------|------|
| Drain-Source Voltage | | V _{DS} | 40 | V |
| Gate-Source Voltage | | V _{GS} | ±20 | V |
| Drain Current | T _C = 25°C | I _D | 65 | A |
| | T _C = 100°C | | 40 | |
| Drain Current Pulsed(Note 1) | | I _{DM} | 260 | A |
| Power Dissipation(T _C =25°C) -Derate above 25°C | | P _D | 50 | W |
| Single Pulsed Avalanche Energy (Note 2) | | E _{AS} | 102 | mJ |
| Operation Junction Temperature Range | | T _J | -55~+150 | °C |
| Storage Temperature Range | | T _{stg} | -55~+150 | °C |
| Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds | | TL | 300 | °C |

THERMAL CHARACTERISTICS

| Characteristics | Symbol | MAX | Unit |
|---|------------------|-----|------|
| Thermal Resistance, Junction-to-Case | R _{θJC} | 4.7 | °C/W |
| Thermal Resistance, Junction-to-Ambient | R _{θJA} | 72 | °C/W |

ELECTRICAL CHARACTERISTICS

| Characteristics | Symbol | Test conditions | Min. | Typ. | Max. | Unit |
|--|---------------------|---|------|------|------|------|
| Off Characteristics | | | | | | |
| Drain -Source Breakdown Voltage | B _{VDS} | V _{GS} =0V, I _D =250μA | 40 | -- | -- | V |
| Drain-Source Leakage Current | I _{DSS} | V _{DS} =40V, V _{GS} =0V | -- | -- | 1 | uA |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} =20V, V _{DS} =0V | -- | -- | 100 | nA |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} =-20V, V _{DS} =0V | -- | -- | -100 | |
| On Characteristics | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | V _{GS} = V _{DS} , I _D =250μA | 1.0 | 1.5 | 2.5 | V |
| Static Drain- Source On State Resistance | R _{DS(on)} | V _{GS} =4.5V, I _D =15A | -- | 7.1 | 10.5 | mΩ |
| | | V _{GS} =10V, I _D =30A | -- | 5.2 | 7.5 | mΩ |
| Dynamic Characteristics | | | | | | |
| Gate Resistance | R _g | V _{GS} =0V; f=1.0MHZ | 1 | 4.0 | 10 | Ω |
| Input Capacitance | C _{iss} | V _{DS} =20V V _{GS} =0V f=1.0MHZ | -- | 2730 | -- | pF |
| Output Capacitance | C _{oss} | | -- | 195 | -- | |
| Reverse Transfer Capacitance | C _{rss} | | -- | 177 | -- | |
| Switching Characteristics | | | | | | |
| Turn-on Delay Time | t _{d(on)} | V _{DD} =20V, V _{GS} =10V R _G =3.0Ω, I _D =25A (Note 3.4) | -- | 9.3 | -- | ns |
| Turn-on Rise Time | t _r | | -- | 18.7 | -- | |
| Turn-off Delay Time | t _{d(off)} | | -- | 23.1 | -- | |
| Turn-off Fall Time | t _f | | -- | 9.6 | -- | |

| | | | | | | |
|--------------------|----------|---|----|------|----|----|
| Total Gate Charge | Q_g | $V_{DS}=20V, I_D=25A$ $V_{GS}=10V$ (Note 3.4) | -- | 43.5 | -- | nc |
| Gate-Source Charge | Q_{gs} | | -- | 8.1 | -- | |
| Gate-Drain Charge | Q_{gd} | | -- | 9.5 | -- | |

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

| Characteristics | Symbol | Test conditions | Min. | Typ. | Max. | Unit |
|---------------------------|----------|---|------|------|------|------|
| Continuous Source Current | I_S | Integral Reverse P-N Junction Diode in the MOSFET | -- | -- | 65 | A |
| Pulsed Source Current | I_{SM} | | -- | -- | 260 | |
| Diode Forward Voltage | V_{SD} | $I_S=30A, V_{GS}=0V$ | -- | 0.85 | 1.4 | V |

1. Pulse width limited by maximum junction temperature
2. $L=0.5mH, V_{DD}=15V, V_G=10V, R_G=25\Omega$, starting $T_J=25^\circ C$
3. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
4. Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. Output Characteristics

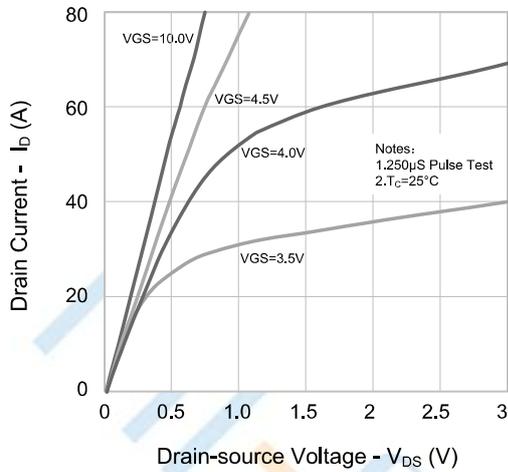


Figure 2. Transfer Characteristics

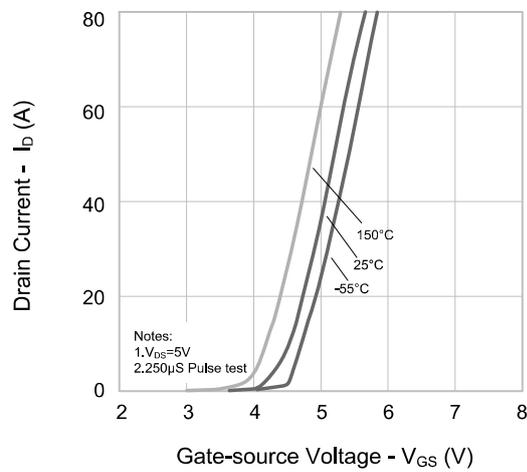


Figure 3. On-resistance vs. Drain Current

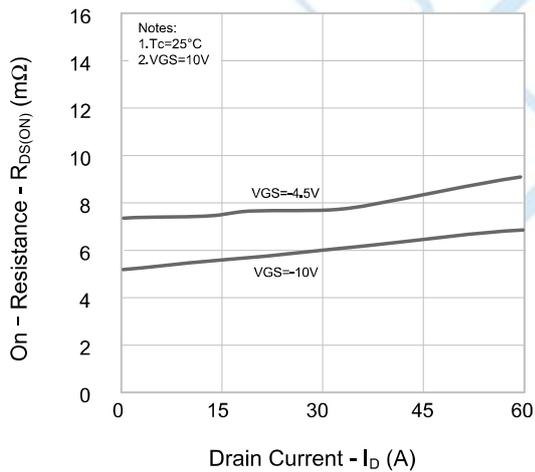


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

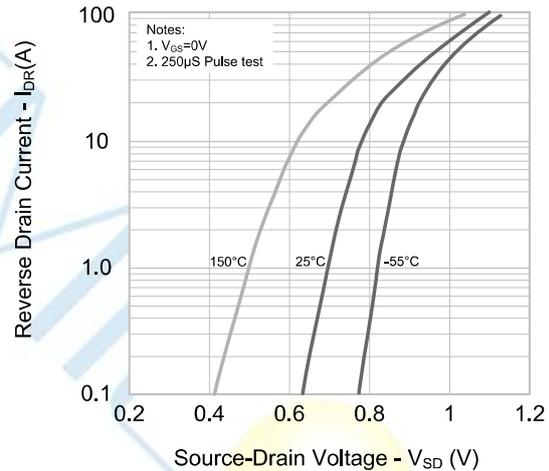


Figure 5. Capacitance Characteristics

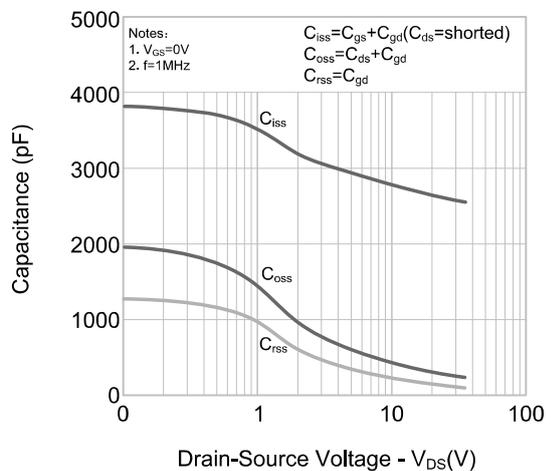
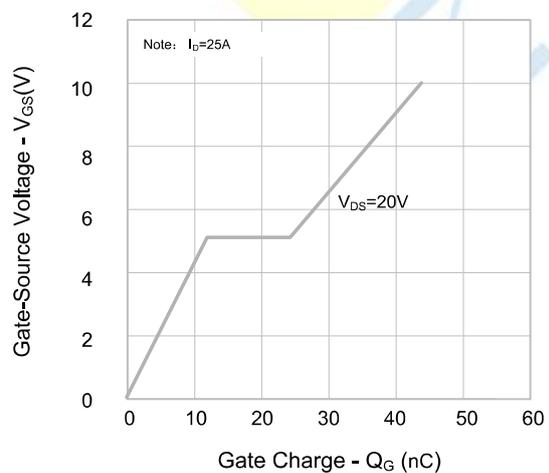


Figure 6. Gate Charge



Typical Performance Characteristics

Figure 7. Breakdown Voltage vs. Temperature Characteristics

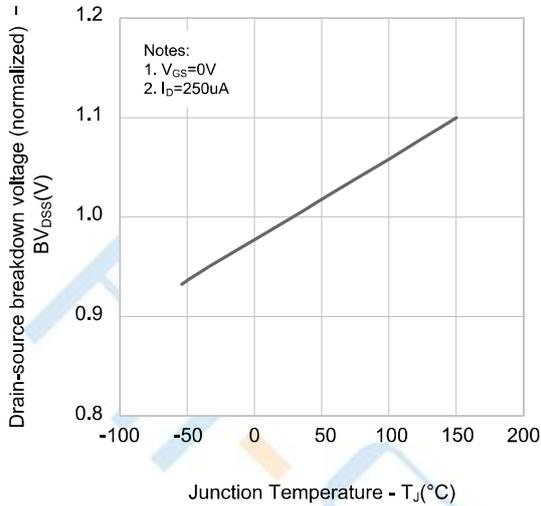


Figure 8. On-resistance vs. Temperature Characteristics

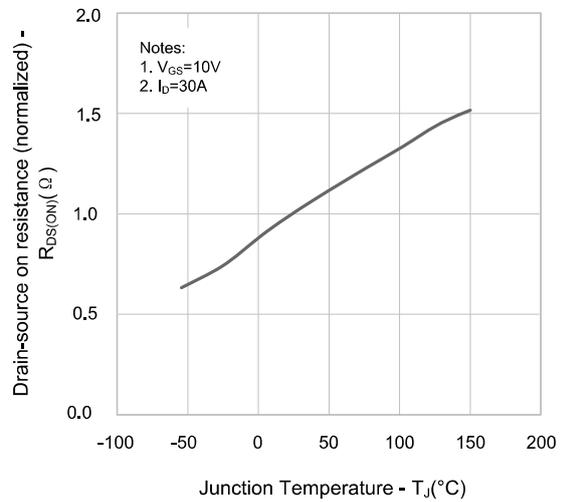
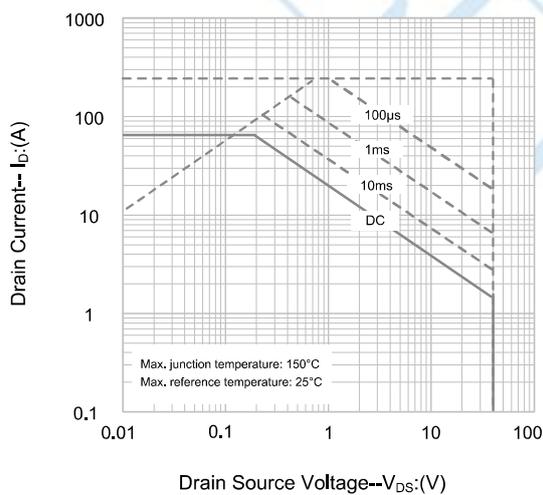
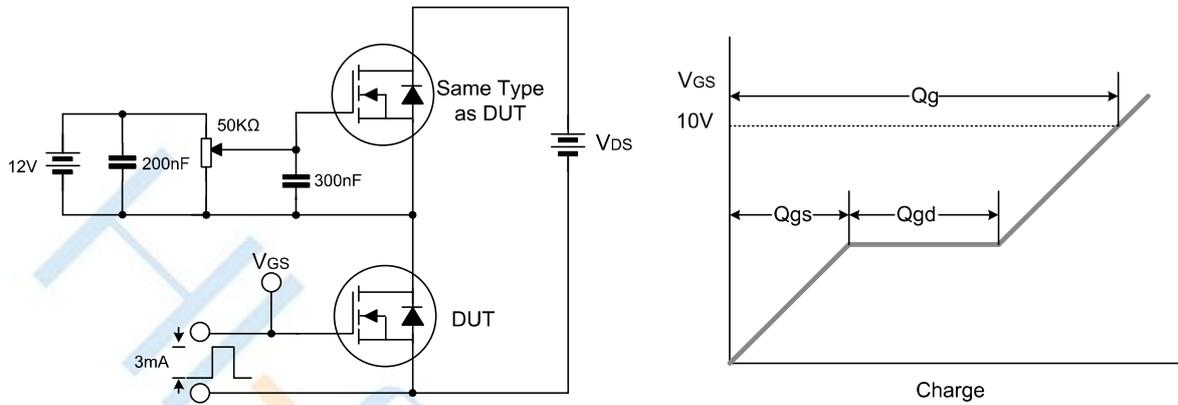


Figure 9. Max. Safe Operating Area

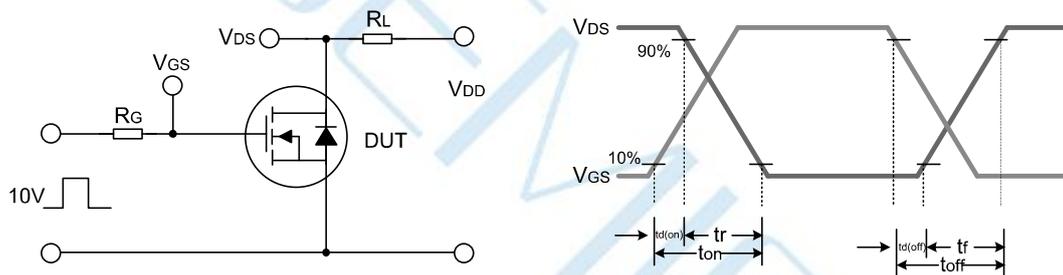


Test Circuit

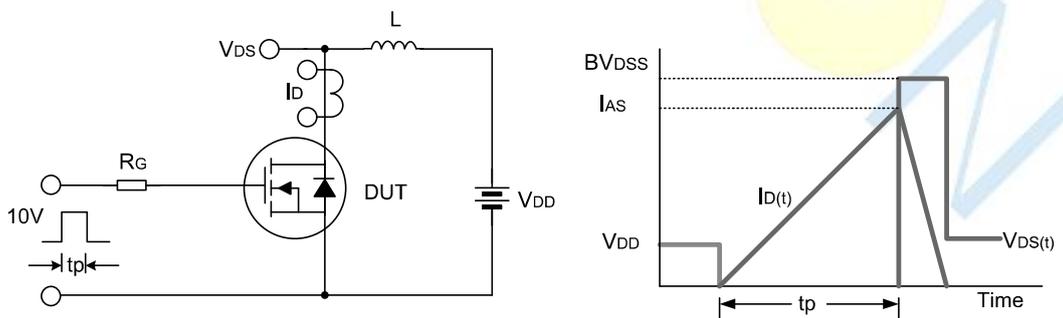
Gate Charge Test Circuit & Waveform



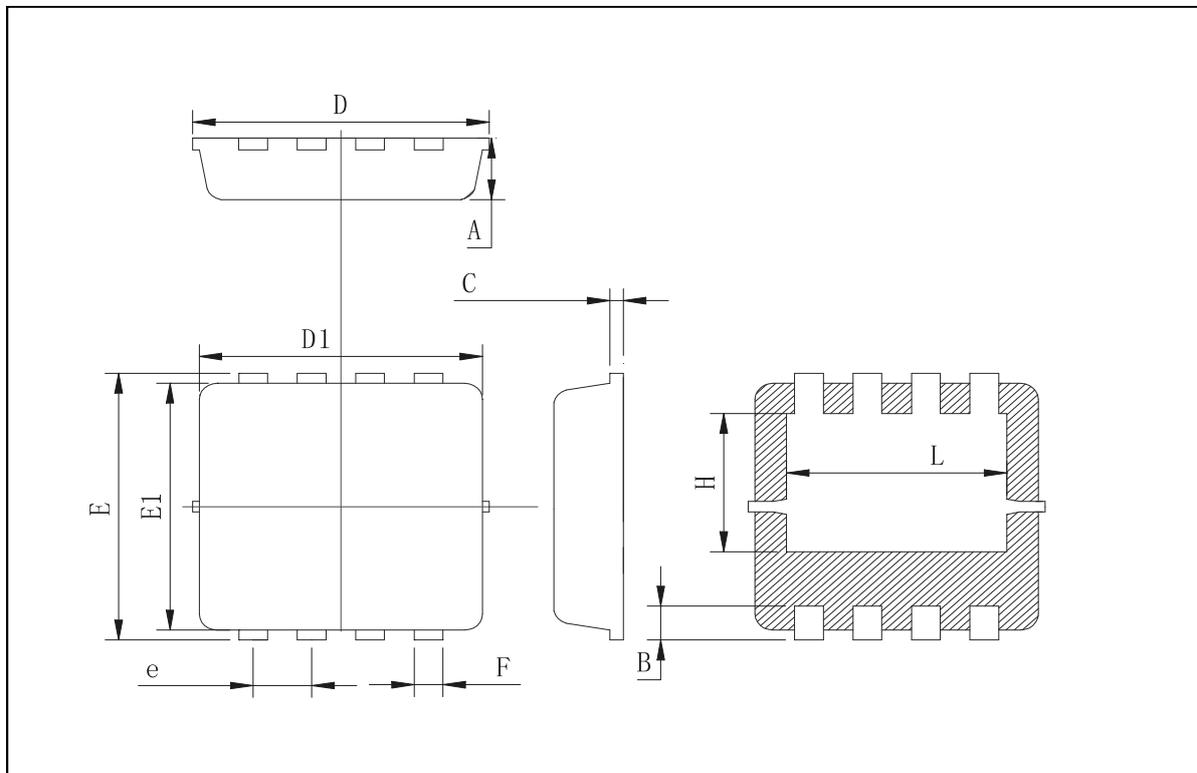
Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform



Package Dimensions of PDFN3*3-8L



Unit:mm

| Symbol | Min | Typ | Max |
|--------|-------|-------|-------|
| A | 0.725 | 0.775 | 0.825 |
| B | 0.28 | 0.38 | 0.48 |
| C | 0.13 | 0.15 | 0.20 |
| D | 3.20 | 3.30 | 3.35 |
| D1 | 3.05 | 3.15 | 3.25 |
| E | 3.25 | 3.35 | 3.45 |
| E1 | 3.0 | 3.1 | 3.2 |
| e | 0.60 | 0.65 | 0.70 |
| F | 0.27 | 0.32 | 0.37 |
| H | 1.63 | 1.73 | 1.83 |
| L | 2.35 | 2.45 | 2.55 |

Disclaimer:

- ▶ Hi-semicon reserves the right to make changes to the information herein for the improvement of the design and performance without further notice! Customers should obtain the latest relevant information before placing orders and should verify that such information is complete and current.
- ▶ All semiconductor products malfunction or fail with some probability under special conditions. When using Hi-semicon products in system design or complete machine manufacturing, it is the responsibility of the buyer to comply with the safety standards strictly and take essential measures to avoid situations in which a malfunction or failure of such Hi-semicon products could cause loss of body injury or damage to property.
- ▶ Hi-semicon will supply the best possible product for customers!