

## -30V, -50A P-CHANNEL POWER MOSFET

### GENERAL DESCRIPTION

The SFN3005PT use advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety applications.

### Features

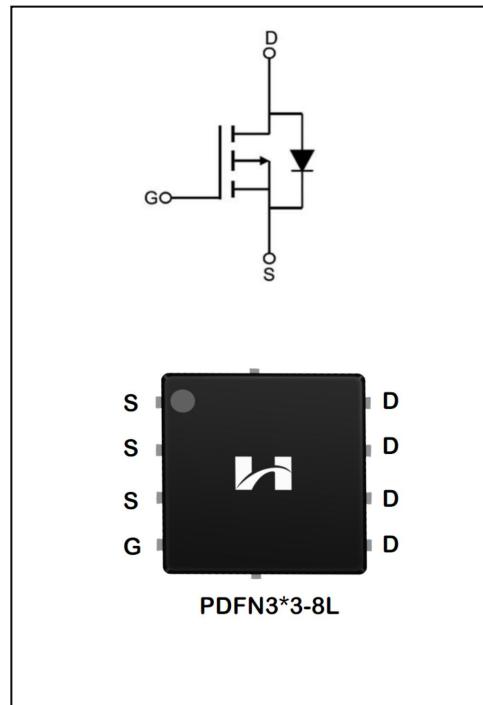
- ◆  $V_{DS} = -30V$ ,  $I_D = -50A$
- ◆  $R_{DS(on)}$

TYP: 9.2mΩ@ $V_{GS} = -10V$

MAX: 11mΩ

### Applications

- ◆ Power factor correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



### ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SFN3005PT	PDFN3*3-8L	SFN3005PT	Pb free	Reel

ABSOLUTE MAXIMUM RATINGS ( $T_J=25^\circ\text{C}$  unless otherwise noted)

Characteristics	Symbol	Ratings	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current	$I_D$	-50	A
$T_C = 100^\circ\text{C}$	$I_D$	-35	
Drain Current Pulsed (Note 1)	$I_{DM}$	-200	A
Power Dissipation( $T_C=25^\circ\text{C}$ )	$P_D$	29.7	W
Operation Junction Temperature Range	$T_J$	-55~+150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	-55~+150	$^\circ\text{C}$
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	TL	300	$^\circ\text{C}$

## THERMAL CHARACTERISTICS

Characteristics	Symbol	MAX	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	4.2	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	75	$^\circ\text{C}/\text{W}$

## ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
Drain -Source Breakdown Voltage	$B_{VDS}$	$V_{GS}=0\text{V}, I_D=-250\mu\text{A}$	-30	--	--	V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=-30\text{V}, V_{GS}=0\text{V}$	--	--	1.0	$\mu\text{A}$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=20\text{V}, V_{DS}=0\text{V}$	--	--	100	$\text{nA}$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=-20\text{V}, V_{DS}=0\text{V}$	--	--	-100	
On Characteristics						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{GS}=V_{DS}, I_D=-250\mu\text{A}$	-1.0	-1.5	-2.5	V
Static Drain- Source On State Resistance	$R_{DS(\text{on})}$	$V_{GS}=-10\text{V}, I_D=-10\text{A}$	--	9.2	11	$\text{m}\Omega$
	$R_{DS(\text{on})}$	$V_{GS}=-4.5\text{V}, I_D=-10\text{A}$	--	13.5	16	
Dynamic Characteristics						
Gate Resistance	$R_g$	$V_{GS}=0\text{V}; f=1.0\text{MHz}$	1	4.5	10	$\Omega$
Input Capacitance	$C_{iss}$	$V_{DS}=-15\text{V}$	--	2202	--	$\text{pF}$
Output Capacitance	$C_{oss}$		--	260.3	--	
Reverse Transfer Capacitance	$C_{rss}$		--	239.6	--	
Switching Characteristics						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-15\text{V}, V_{GS}=-10\text{V}$ $R_G=1.0\Omega, I_D=-10.0\text{A}$ (Note 2.3)	--	11.2	--	$\text{ns}$
Turn-on Rise Time	$t_r$		--	6.4	--	
Turn-off Delay Time	$t_{d(off)}$		--	75.8	--	
Turn-off Fall Time	$t_f$		--	14.8	--	

Total Gate Charge	$Q_g$	$V_{DS}=-15V, I_D=-10.0A$ $V_{GS}=-10V$ (Note 2.3)	--	39.2	--	nc
Gate-Source Charge	$Q_{gs}$		--	5.6	--	
Gate-Drain Charge	$Q_{gd}$		--	7.4	--	

## SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	$I_s$	Integral Reverse P-N Junction Diode in the MOSFET	--	--	-50	A
Pulsed Source Current	$I_{SM}$		--	--	-200	
Diode Forward Voltage	$V_{SD}$	$I_s=-20A, V_{GS}=0V$	--	--	1.4	V

1.Pulse width limited by maximum junction temperature

2.Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ 

3.Essentially independent of operating temperature

### Typical Performance Characteristics

Fig 1: Output Characteristics

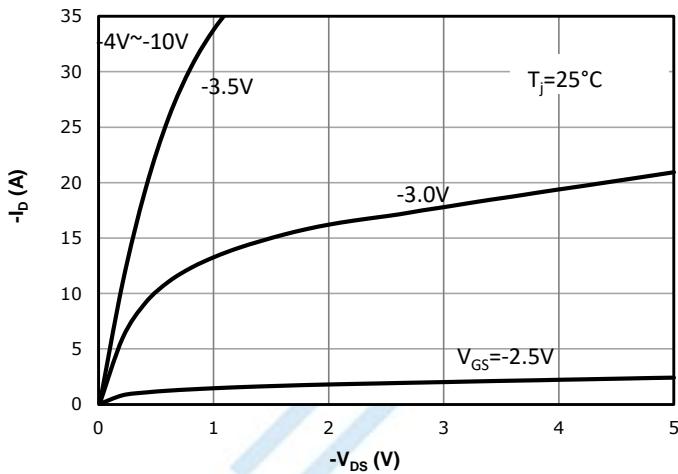


Fig 2: Transfer Characteristics

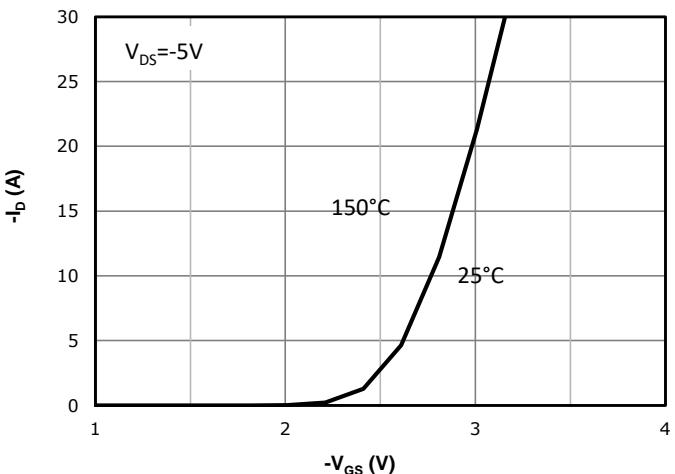


Fig 3:  $R_{DS(on)}$  vs Drain Current and Gate Voltage

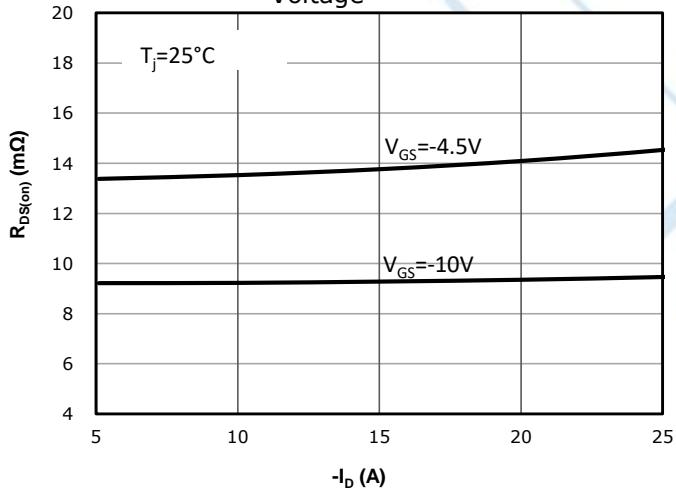


Fig 4:  $R_{DS(on)}$  vs Gate Voltage

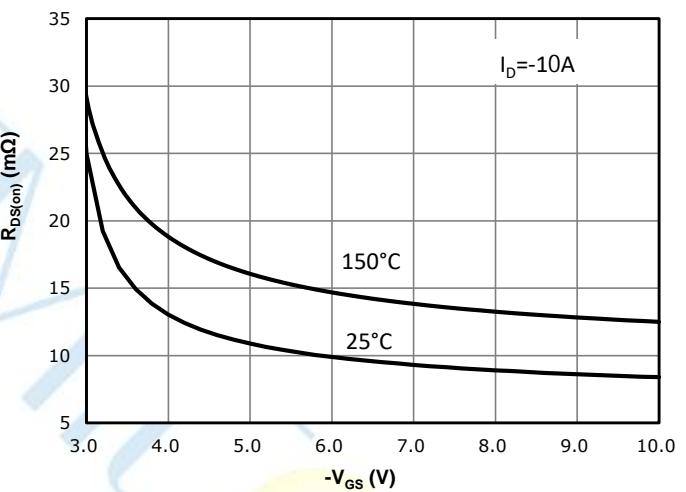


Fig 5:  $R_{DS(on)}$  vs. Temperature

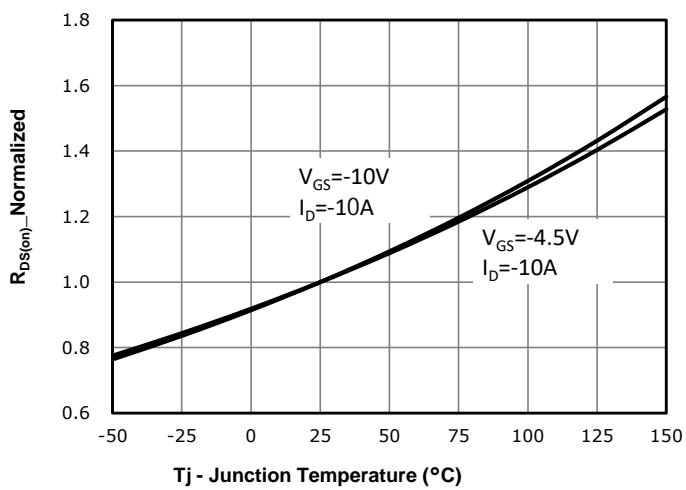
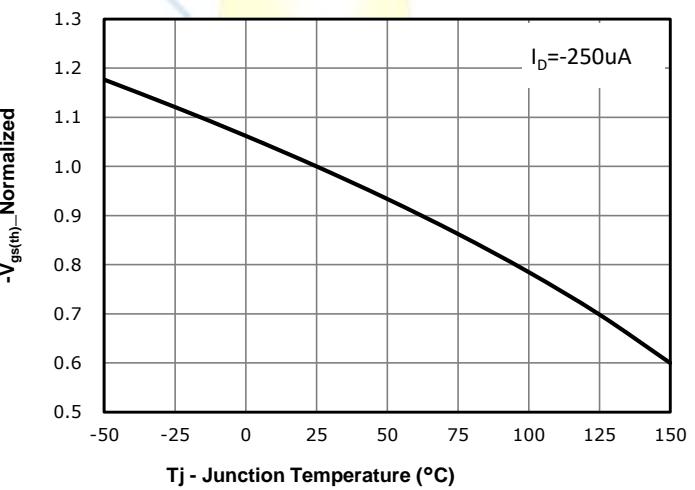


Fig 6:  $V_{GS(th)}$  vs. Temperature



### Typical Performance Characteristics

Fig 7: BV<sub>DSS</sub> vs. Temperature

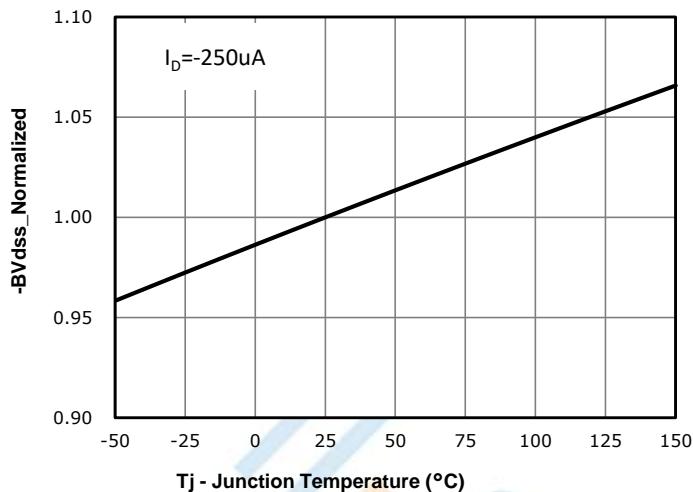


Fig 8: Body-diode Forward Characteristics

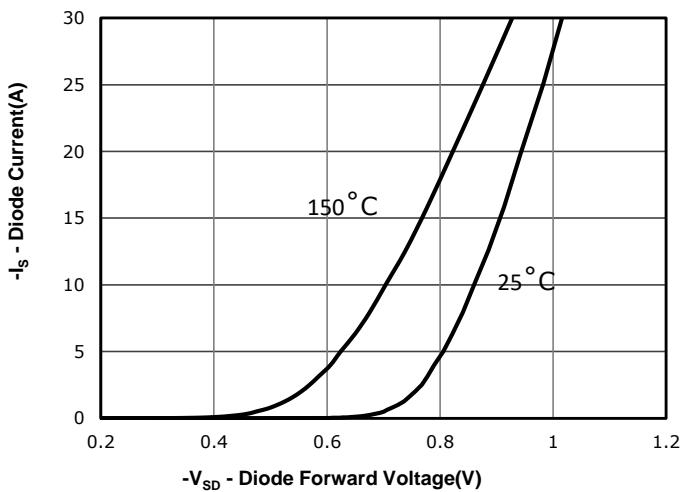


Fig 9: Gate Charge Characteristics

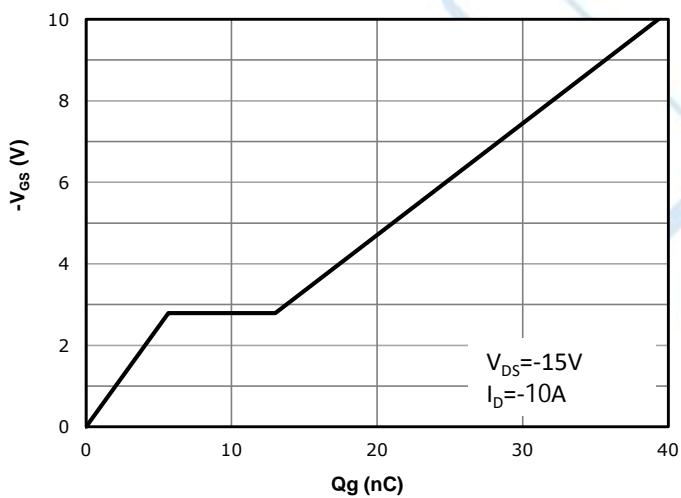


Fig 10: Capacitance Characteristics

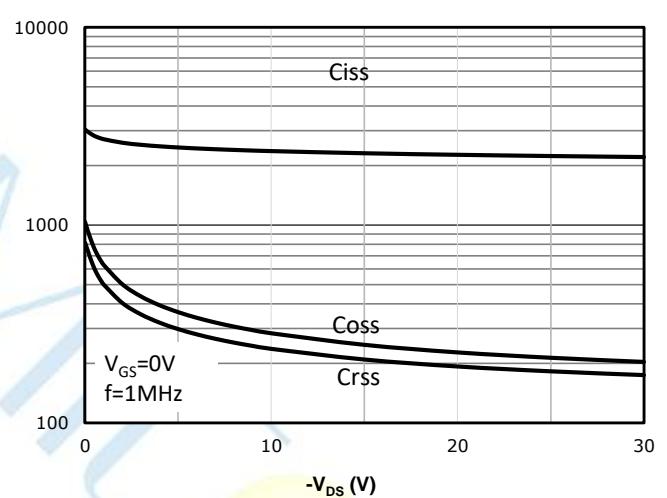


Fig 11: Drain Current Derating

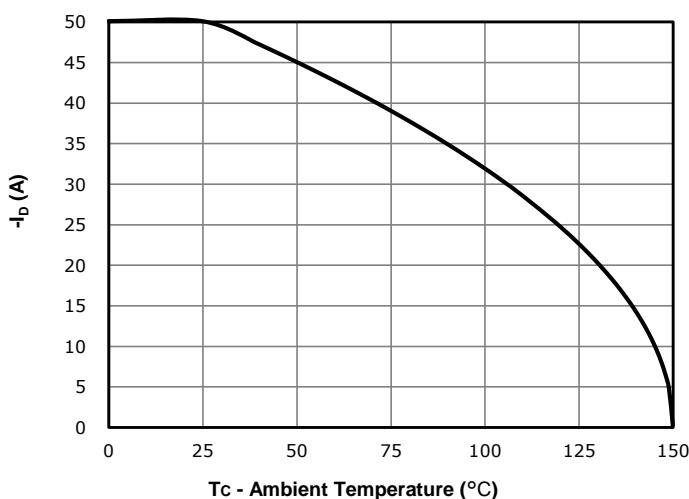
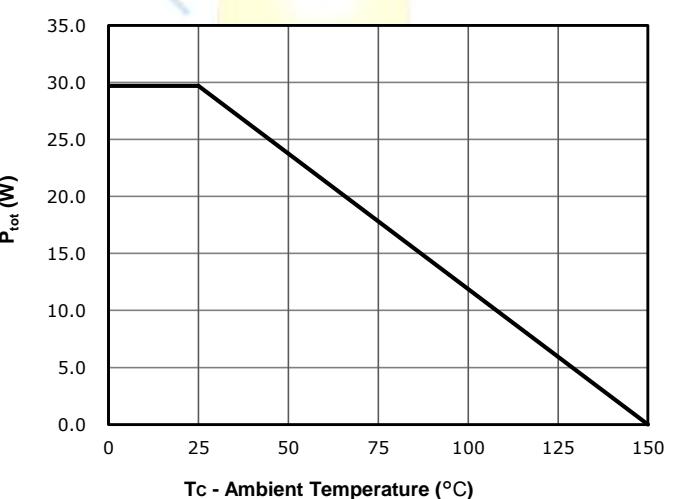
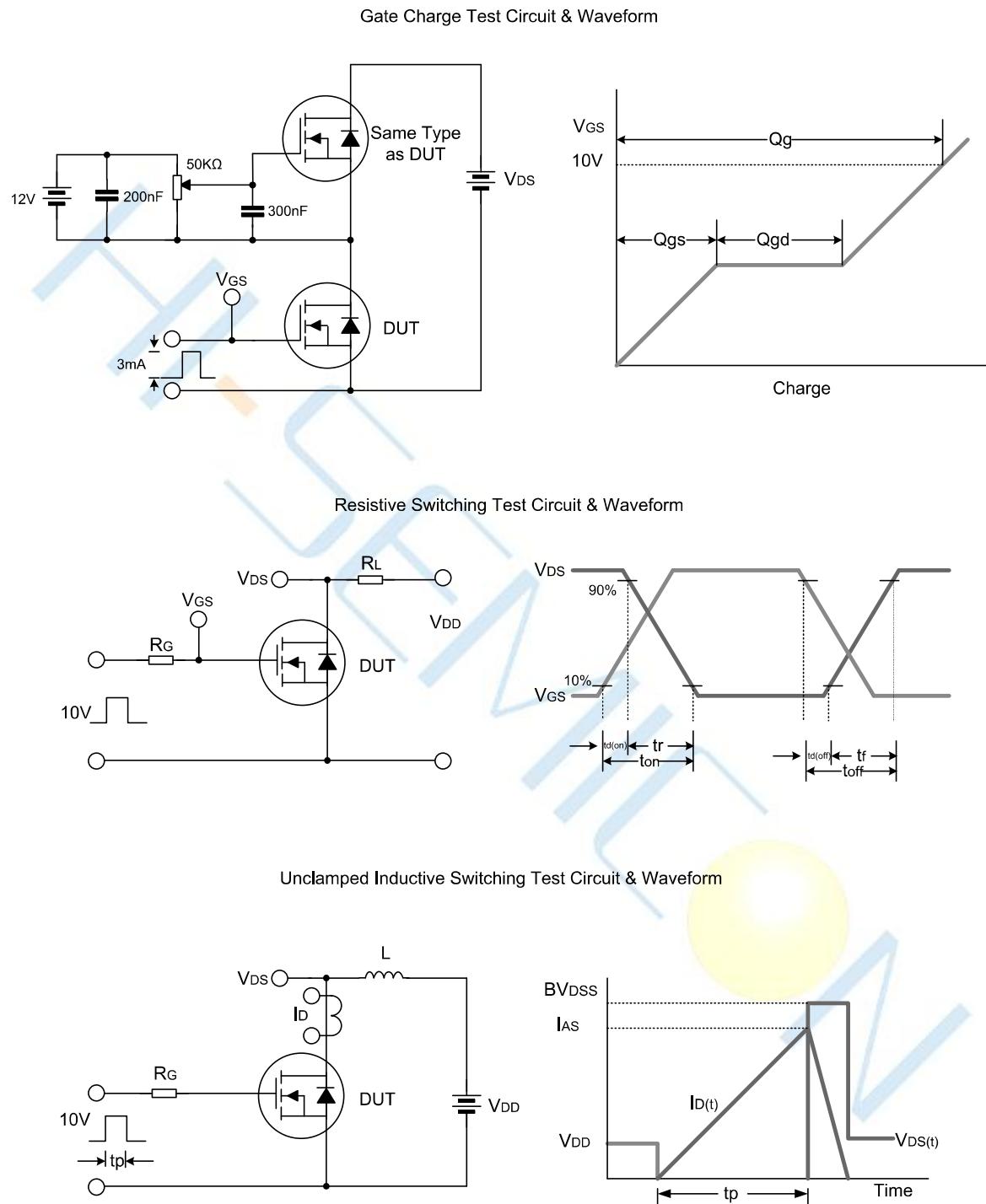


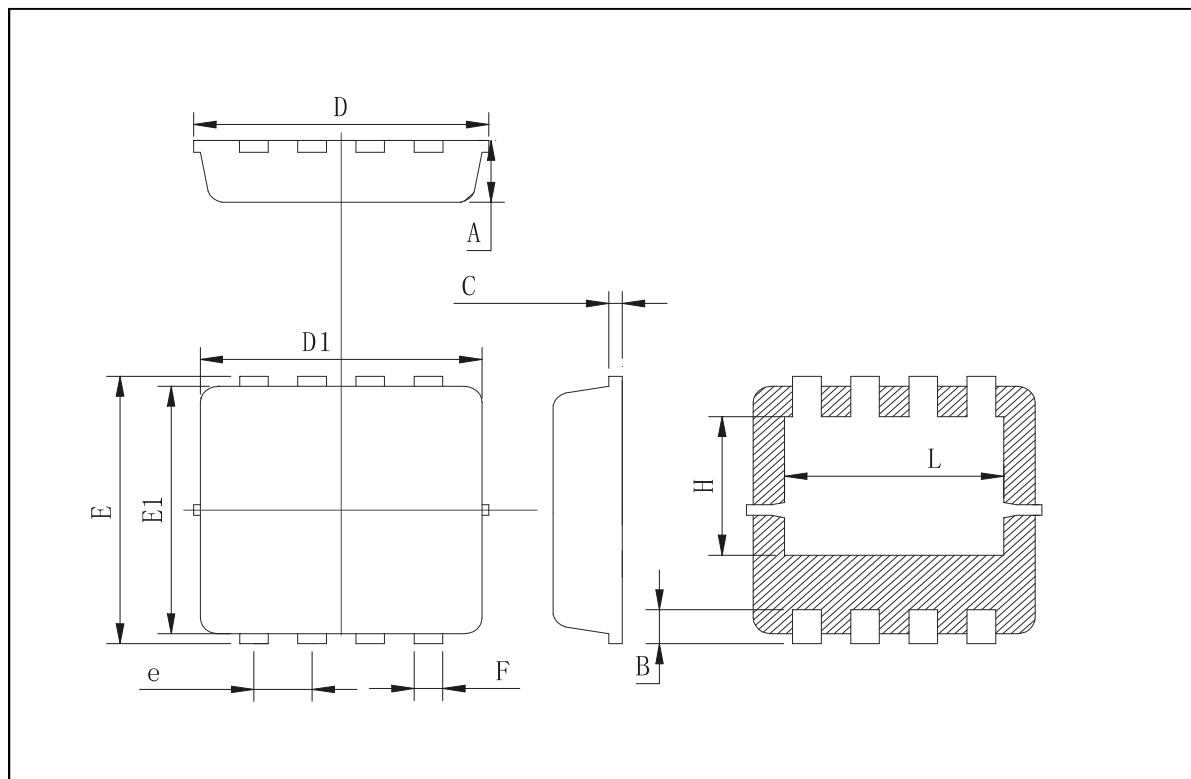
Fig 12: Power Dissipation



## Test Circuit



## Package Dimensions of PDFN3\*3-8L

**Unit:mm**

Symbol	Min	Typ	Max
A	0.725	0.775	0.825
B	0.28	0.38	0.48
C	0.13	0.15	0.20
D	3.20	3.30	3.35
D1	3.05	3.15	3.25
E	3.25	3.35	3.45
E1	3.0	3.1	3.2
e	0.60	0.65	0.70
F	0.27	0.32	0.37
H	1.63	1.73	1.83
L	2.35	2.45	2.55

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