

120A, 30V N-CHANNEL POWER MOSFET

GENERAL DESCRIPTION

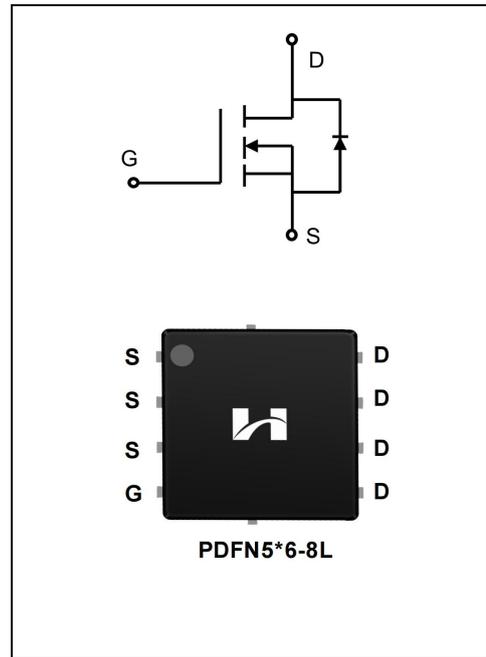
The SFM3012T uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety applications.

Features

- ◆  $V_{DS}=30V, I_D=120A$
- ◆  $R_{DS(on)}$   
TYP:  $2.4m\Omega @ V_{GS}=10V, I_D=30A$

Applications

- ◆ Power faction correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SFM3012T	PDFN-5*6-8L	SFM3012T	Pb Free	Reel

## ABSOLUTE MAXIMUM RATINGS (T<sub>J</sub>=25°C unless otherwise noted)

Characteristics	Symbol	Ratings	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current	I <sub>D</sub>	T <sub>C</sub> = 25°C	120
		T <sub>C</sub> = 100°C	81
Drain Current Pulsed(Note 1)	I <sub>DM</sub>	480	A
Power Dissipation(T <sub>C</sub> =25°C) -Derate above 25°C	P <sub>D</sub>	81	W
Single Pulsed Avalanche Energy (Note 2)	E <sub>AS</sub>	338	mJ
Operation Junction Temperature Range	T <sub>J</sub>	-55~+150	°C
Storage Temperature Range	T <sub>stg</sub>	-55~+150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	TL	300	°C

## THERMAL CHARACTERISTICS

Characteristics	Symbol	MAX	Unit
Thermal Resistance, Junction-to-Case	R <sub>θJC</sub>	1.65	°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	55	

## ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
Drain -Source Breakdown Voltage	B <sub>VDS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	--	--	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	--	--	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =20V, V <sub>DS</sub> =0V	--	--	100	nA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V	--	--	-100	
On Characteristics						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> =250μA	1.0	1.5	2.0	V
Static Drain- Source On State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	--	2.4	3.1	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A	--	3.5	4.5	
Dynamic Characteristics						
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, f=1.0MHZ	1	3.1	10	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V V <sub>GS</sub> =0V f=1.0MHZ	--	4160	--	pF
Output Capacitance	C <sub>oss</sub>		--	442	--	
Reverse Transfer Capacitance	C <sub>rss</sub>		--	406	--	
Switching Characteristics						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =15V R <sub>G</sub> =3.3Ω, I <sub>D</sub> =30A (Note 3.4)	--	22.1	--	ns
Turn-on Rise Time	t <sub>r</sub>		--	18.7	--	
Turn-off Delay Time	t <sub>d(off)</sub>		--	63.5	--	
Turn-off Fall Time	t <sub>f</sub>		--	12.6	--	

Total Gate Charge	$Q_g$	$V_{DS}=15V, I_D=30A$ $V_{GS}=10V$ (Note 3.4)	--	75.6	--	nc
Gate-Source Charge	$Q_{gs}$		--	8.5	--	
Gate-Drain Charge	$Q_{gd}$		--	20.6	--	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	$I_S$	Integral Reverse P-N Junction Diode in the MOSFET	--	--	120	A
Pulsed Source Current	$I_{SM}$		--	--	480	
Diode Forward Voltage	$V_{SD}$	$I_S=30A, V_{GS}=0V$	--	0.8	1.2	V
Reverse Recovery Time	$T_{rr}$	$I_F=30A$ $dI_F/dt=100A/\mu S$	--	62	--	ns
Reverse Recovery Charge	$Q_{rr}$		--	0.15	--	$\mu C$

- 1.Pulse width limited by maximum junction temperature
- 2.L=0.5mH,  $V_{DD}=15V, V_G=10V, R_G=25\Omega$ , starting  $T_J=25^\circ C$
- 3.Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$
- 4.Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. Output Characteristics

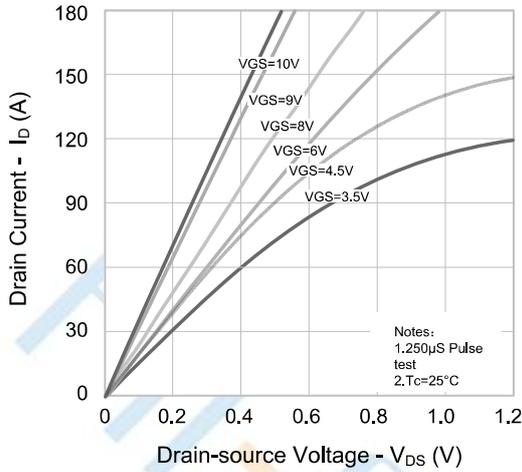


Figure 2. Transfer Characteristics

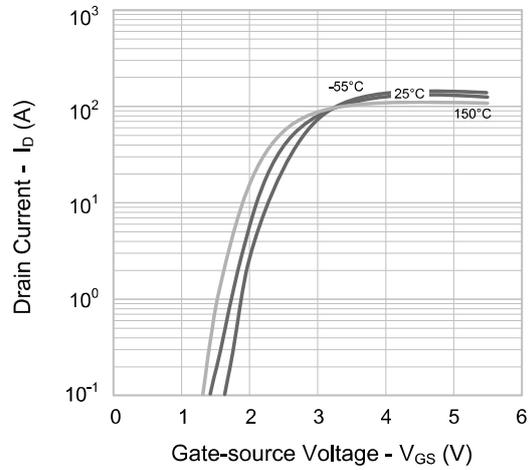


Figure 3. On-Resistance vs. Drain Current

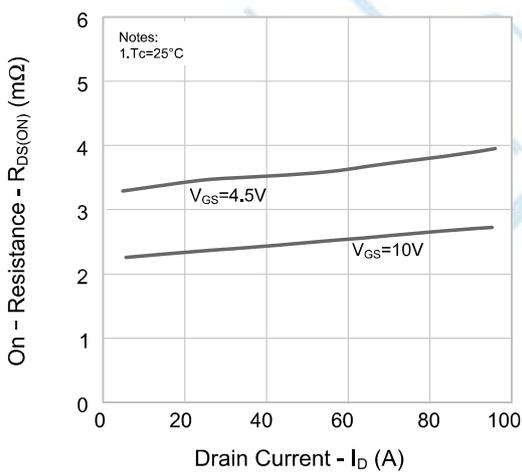


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

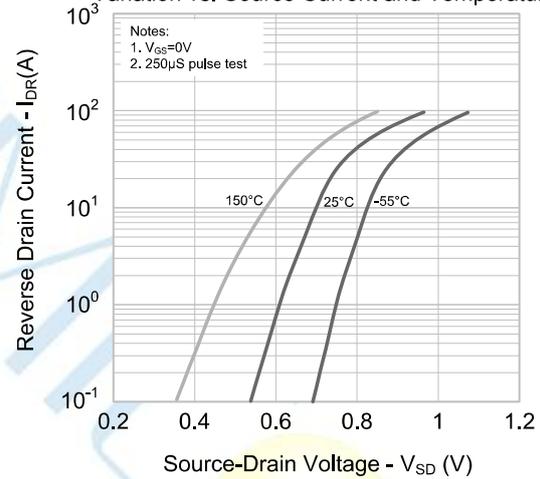


Figure 5. Capacitance Characteristics

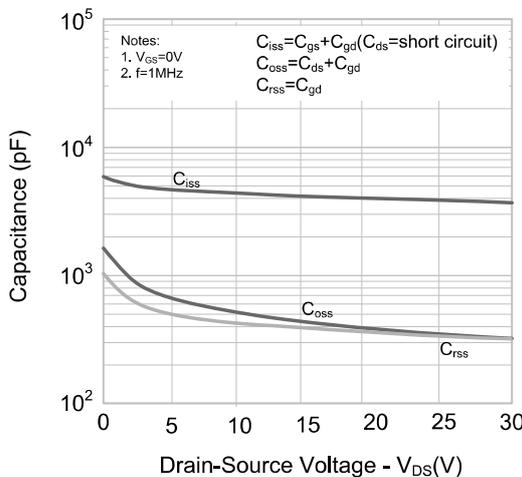
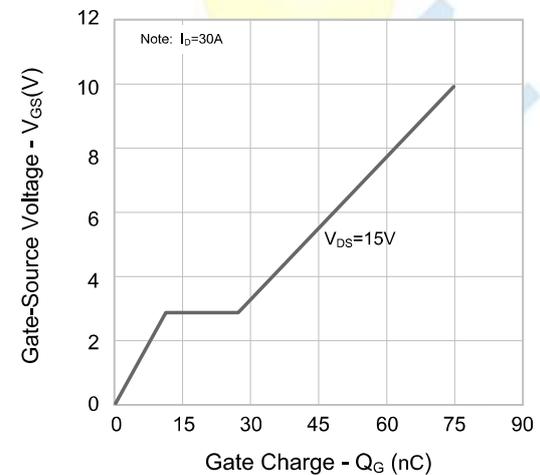
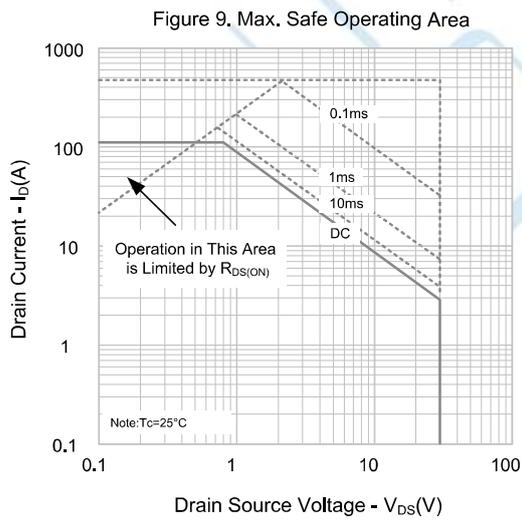
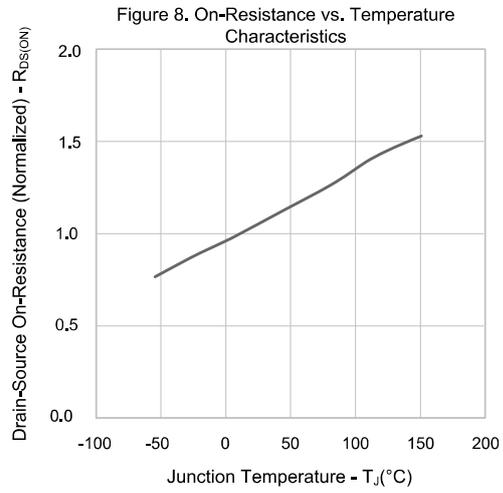
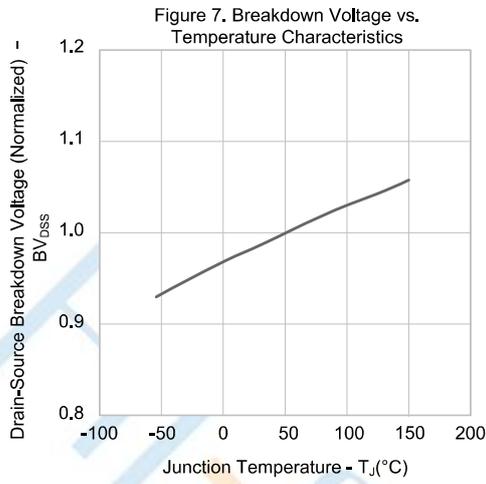


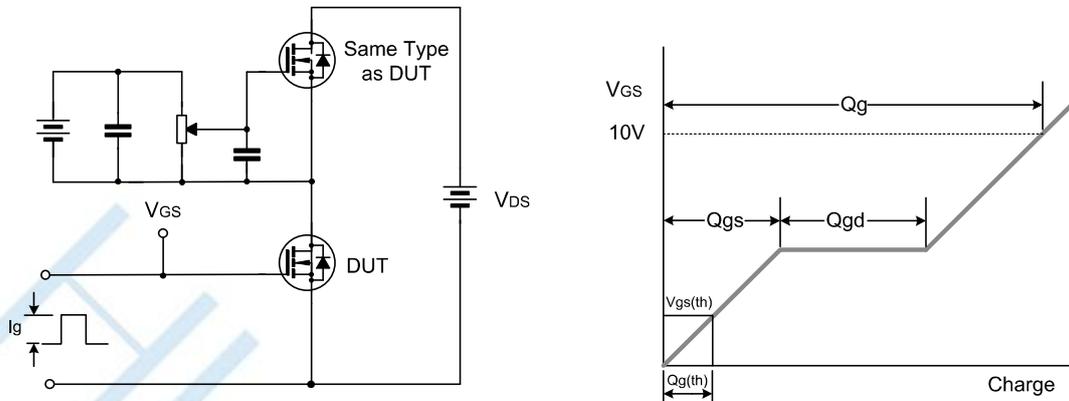
Figure 6. Gate Charge



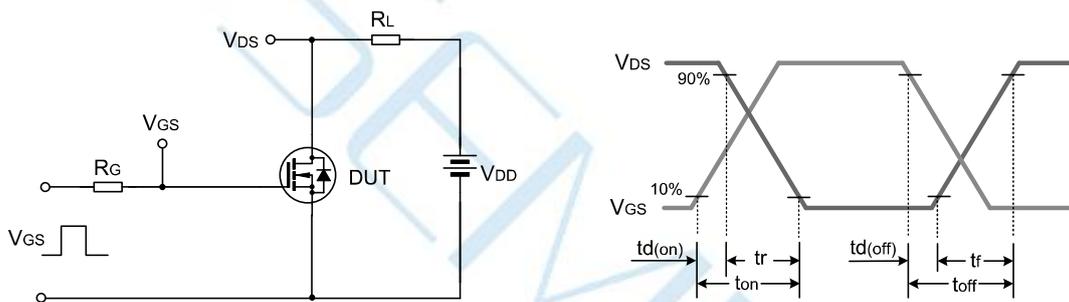
Typical Performance Characteristics



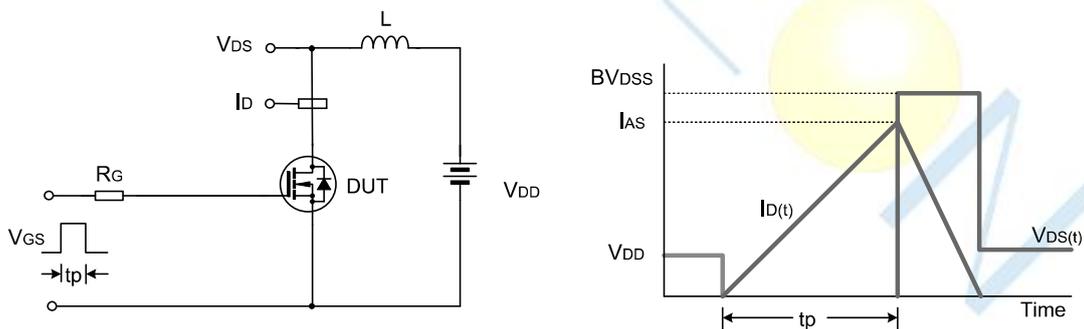
Test Circuit



Gate Charge Test Circuit & Waveform



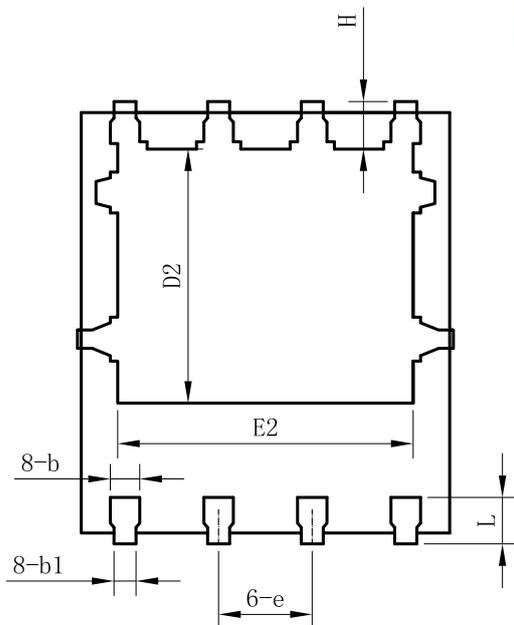
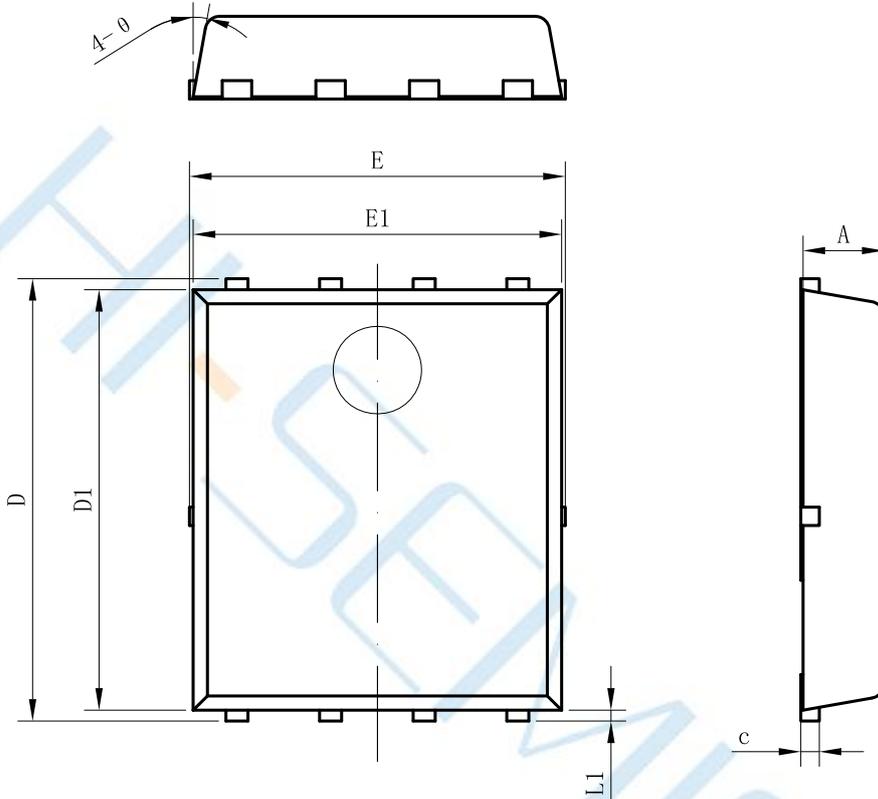
Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform

Package Dimensions of PDFN5\*6-8L

Unit:mm



COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	1.0	1.1	1.2
b	0.35	0.4	0.45
b1		(0.3)	
c	0.2	0.25	0.35
D	5.9	6.05	6.2
D1	5.65	5.75	5.85
D2		(3.475)	
E			5.2
E1	4.9	5	5.1
E2		(4.01)	
e		1.27BSC	
H	0.5	0.65	0.75
L	0.51	0.635	0.75
L1		0.15	
θ		10°	

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